

**REMARKS**

The above-identified patent application has been amended and reconsideration and re-examination are hereby requested.

Claim 2 has been amended to point out that an interrupt request controller for processing a plurality of interrupt logic signals, such controller comprises a programmable section fed by the interrupt signals, for selecting assertion sense of each one of the interrupt signals. It is respectfully submitted that neither Kardach et al (USP 5,535,420) Culley et al. (USP 5,101,497) nor Nelson et al. (USP 5,819,096) describe such feature.

Claim 3 points out that the interrupt controller comprises a programmable section fed the interrupt signals, for storing a bit for each one of the interrupt logic signals representative of whether the logic state of the interrupt logic signal should be, or should not be, inverted and for producing a corresponding output logic interrupt signal in accordance therewith. (emphasis added). It is respectfully submitted that neither Kardach et al (USP 5,535,420) Culley et al. (USP 5,101,497) nor Nelson et al. (USP 5,819,096) describe such feature. Nothing in these patents describe a programmable section as claimed.

Claim 4 has been cancelled.

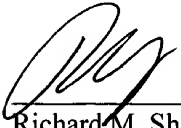
New claim 10 points out that the interrupt request controller comprises a programmable section fed by the interrupt signals, for selecting assertion sense and assertion type of each one of the interrupt signals. (Emphasis ours) It is respectfully submitted that neither Kardach et al (USP 5,535,420) Culley et al. (USP 5,101,497) nor Nelson et al. (USP 5,819,096) describe such feature

It is noted that paragraph 6 of the Office action indicates that claims 1 and 5-9 are allowable. It is also noted that the Office Action Summary sheet fails to indicate in box 5 the allowance of claim 1.

In the event any additional fee is required, please charge such amount to Patent and  
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Respectfully submitted,

5-30-03  
Date

  
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